

## Electronic Acknowledgement Receipt

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<b>Application Number:</b>	10728492
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9752
<b>Title of Invention:</b>	Low power memory controller with leaded double data rate DRAM package arranged on a two layer printed circuit board
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<b>Application Type:</b>	Utility

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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	US_IDS_Form__SB_08a.pdf	662262	no	4
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2	NPL Documents	JEDEC_standard.pdf	8100807	no	85
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3	NPL Documents	Bakoglu_pg_321.pdf	116315	no	3
<b>Warnings:</b>					
<b>Information:</b>					
4	Fee Worksheet (PTO-06)	fee-info.pdf	8239	no	2
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**New International Application Filed with the USPTO as a Receiving Office**

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